

3714 U.S. PTO  
10/06/24  
02/07/02

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10067424	FILING DATE 02/07/2002	CLASS 257	SUBCLASS 1003	GAU 2815	EXAMINER Brook
<b>**APPLICANTS:</b> Lu Jeng; Mei Ping; Lemmi Francesco; Street Robert; Boyce James;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/419,293 10/15/1999					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 7447.0021-01	
TITLE : Dual dielectric structure for suppressing lateral leakage current in high fill factor arrays <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-435L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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